

### **REMARKS**

Applicants thank the Examiner for the thorough consideration given the present application. Claims 1, 2, 6 and 7 are currently being prosecuted. The Examiner is respectfully requested to reconsider his rejection in view of the amendments and remarks as set forth below.

### **Interview**

Applicants note with appreciation the interview conducted on August 31, 2005 with Examiners Shapiro and Shankar. At that interview, the references were discussed, along with possible additional claim language. Applicants are now submitting the present amendment for full consideration.

### **Rejection Under 35 U.S.C. § 103**

Claims 1, 2, 6 and 7 stand rejected under 35 U.S.C. § 103(a) as being obvious over the Admitted Prior Art in view of Herman (U. S. Patent 4,325,063) and Taguchi et al. (U. S. Patent 6,181,317). This rejection is respectfully traversed.

The Examiner points out that the Admitted Prior Art teaches a method of processing signals including the steps of receiving a vertical synchronizing signal, receiving a data enable signal, generating a gate clock signal, wherein, after a rising edge or falling edge of the vertical synchronizing signal, a plurality of gate-on enable signals are generated, and wherein the start vertical signals include STV1 and STV2. The Examiner admits that the Admitted Prior Art does

not show generating the start vertical signals before the end of the vertical blank period after a gate clock cycle during the vertical blank period.

The Examiner relies on Taguchi et al. to teach the generation of the start vertical signals before the end of the vertical blank period after at least a gate clock cycle. The Examiner feels it would have been obvious to one of ordinary skill in the art to incorporate the teachings of Taguchi et al. in regard to the timing signals into the Admitted Prior Art to drive the display.

Applicants disagree that the claims would be obvious over the combination of the Admitted Prior Art and Taguchi et al. While Taguchi et al. may show a series of signals having a similar timing to that described, there would be no motivation for one skilled in the art to utilize this arrangement in the Admitted Prior Art device. Taguchi et al. utilizes this arrangement in order to increase the frequency of the clock signal so that the size of the display may be changed, such as from 4 x 3 to 16 x 9. The particular arrangement of the timing signals is merely fortuitous and is not for any purpose such as described in the present invention. Accordingly, Applicants submit that it would not be obvious to utilize this arrangement in the Admitted Prior Art.

Furthermore, the Examiner states that even the combination of the Admitted Prior Art and Taguchi et al. does not show the pausing of the output of the control signals until the end of the vertical blank period. The Examiner relies on Herman to teach the pausing of the OE signal until the end of the vertical blank period. The Examiner feels that it would have been obvious to pause the output of the signals, as shown by Herman.

First, Applicants disagree with the Examiner's understanding of the Herman reference. In Herman, the character code source stores a page of data to be displayed. One line of this page is alternately inserted into memory VLMY and VLMX. While one memory is being placed in the display device, under the control of signal OE, the other memory is being loaded. When the first unit finishes, the OE signal ends and the corresponding signal OE' then activates the other memory unit to send one line to the display device. The original memory unit is then loaded with another line for a later display. The Examiner has stated that this reference shows that the signal OE is paused. However, it is clear from column 3, lines 39 and following that the signal on line OE instead terminates so that the corresponding signal on line OE' can be implemented. Thus, the OE signal is not delayed, but completely stopped until the next line is loaded. Applicants submit that the Herman reference does not teach the delay of the OE signal and, furthermore, that there would be no motivation for one skilled in the art to utilize this type of signal arrangement in the Admitted Prior Art. Accordingly, Applicants submit that claim 1 is allowable.

Claim 6 is similar to claim 1 regarding these arguments against the Admitted Prior Art. Accordingly, Applicants submit that claim 6 is likewise allowable.

Claims 2 and 7 depend from these allowable claims and as such are also considered to be allowable. In addition, these claims recite that the vertical signals are generated after at least a third cycle during the vertical blank period. Applicants submit that this feature is also not seen in the references and that, accordingly, these claims are additionally allowable.

Furthermore, Applicants have now added an additional statement at the end of each of claims 1 and 6. These statements relate to the fact that the result of this method is to process the control signals in real time so that real time driving is achieved. These statements are found in the specification at page 3, lines 1 and 2, page 3, line 17, and page 5, line 15. Applicants submit that none of the references, nor their combination, show these features either. Accordingly, Applicants submit that these claims are additionally allowable.

### **Conclusion**

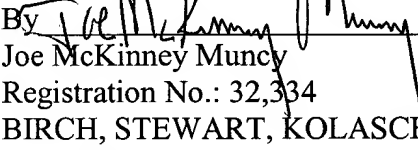
In view of the above amendments and remarks, it is believed that the claims clearly distinguish over the patents relied on by the Examiner, either alone or in combination. In view of this, reconsideration of the rejection and allowance of all of the claims are respectfully requested.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Robert F. Gnuse (Reg. No. 27,295) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

  
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